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This book represents an attempt to treat three aspects of digital systems, design, prototyping and customization, in an integrated manner using two major technologies: VHSIC Hardware Description Language (VHDL) as a modeling and specification tool, and Field-Programmable Logic Devices (FPLDs) as an implementation technology.

VHDL and FPLDs in Digital Systems Design, Prototyping and

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The two technologies are VHSIC Hardware Description Language (VHDL) and Field-Programmable Logic Devices (FPLDs). VHDL is used for modeling and specification; FPLDs are used for implementation. VHDL and FPLDs in Digital Systems Design, Prototyping and Customization is divided into three parts.

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Further examples of VHDL descriptions which result in valid synthesized circuits for Altera FPLDs are also presented. More complex examples and features of Altera's VHDL implementation and its use in design for FPLDs can be found in the later chapters of Part 3.

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VHDL DESIGN AND FPLD IMPLEMENTATION FOR SILICON TRACK CLUSTER CARD By SHWETA LOLAGE ... (VHSIC) Hardware Description Language (VHDL) is ... The MAXPLUS II synthesis tool by ALTERA Corporation was used to implement the design in FPLDs. The final design is

VHDL DESIGN AND FPLD IMPLEMENTATION FOR SILICON TRACK

VHDL is a hardware description language (HDL) that contains the features of conventional programming languages such as Pascal or C, logic description languages such as ABEL-HDL, and netlist languages such as EDIF. VHDL also includes design management features, and

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